

Appl. No. 10/720,812  
Amdt. Dated June 29, 2005

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1. (withdrawn) A method comprising:  
integrating an inductor on a spacer between upper and lower dies in stacked dies; and  
attaching conductors to electrically connect the inductor to one of the upper and lower dies.
2. (withdrawn) The method of claim 1 further comprising:  
filling adhesive between the spacer and the upper die and between the spacer and the lower die.
3. (withdrawn) The method of claim 1 wherein integrating comprises:  
integrating the inductor being a thin-film inductor.
4. (withdrawn) The method of claim 1 wherein integrating comprises:  
integrating the inductor having a thickness substantially less than thickness of the spacer.
5. (withdrawn) The method of claim 4 wherein integrating comprises:  
integrating the inductor having a multi-turn geometry.
6. (withdrawn) The method of claim 1 wherein integrating comprises:  
integrating the inductor having an inductance of approximately between 1 nH to 10 nH.
7. (withdrawn) The method of claim 1 wherein attaching the conductors comprises:  
attaching bumps to electrically connect the inductor to at least one of the upper and lower dies.
8. (withdrawn) The method of claim 1 further comprising:  
filling adhesive between the lower die to a package substrate.

Appl. No. 10/720,812  
Amdt. Dated June 29, 2005

9. (currently amended) A spacer assembly comprising:  
a passive element inductor integrated on a spacer between upper and lower dies in stacked dies; and  
conductors attached to the passive element inductor to connect the passive element inductor to at least one of the upper and lower dies.
10. (original) The spacer assembly of claim 9 further comprising:  
adhesive layers filled between the spacer and the upper die and between the spacer and the lower die.
11. (currently amended) The spacer assembly of claim 9 wherein the passive element inductor is a thin-film element inductor.
12. (currently amended) The spacer assembly of claim 9 wherein the passive element inductor has a thickness substantially less than thickness of the spacer.
13. (currently amended) The spacer assembly of claim 12 wherein the passive element inductor has a multi-turn geometry.
14. (currently amended) The spacer assembly of claim 13 ~~[[9]]~~ wherein the passive element is an inductor having has an inductance of approximately between 1 nH to 10 nH, or a resistor having a resistance of approximately between 0.2 ohms to 2.0 ohms.
15. (currently amended) The spacer assembly of claim 9 wherein the conductors comprises:  
bumps attached to the passive element inductor to electrically connect the passive element inductor to at least one of the upper and lower dies.
16. (original) The spacer assembly of claim 9 wherein the lower die is attached to a package substrate by an adhesive between the lower die and the package substrate.
17. (currently amended) A die assembly comprising:

Appl. No. 10/720,812  
Amdt. Dated June 29, 2005

a package substrate;  
a plurality of stacked dies on the package substrate and having at least an upper die and a lower die; and  
at least a spacer assembly between the upper and lower dies, the spacer assembly comprising:

a passive element inductor integrated on a spacer between the upper and lower dies, and  
conductors attached to the inductor to electrically connect the passive element inductor to at least one of the upper and lower dies.

18. (original) The die assembly of claim 17 wherein the spacer assembly further comprises:

adhesive layers filled between the spacer and the upper die and between the spacer and the lower die.

19. (currently amended) The die assembly of claim 17 wherein the passive element inductor is a thin-film element inductor.

20. (currently amended) The die assembly of claim 17 wherein the passive element inductor has a thickness substantially less than thickness of the spacer.

21. (currently amended) The die assembly of claim 20 wherein the passive element inductor has a multi-turn geometry.

22. (currently amended) The die assembly of claim 21 [[17]] wherein the passive element is an inductor having has an inductance of approximately between 1 nH to 10 nH, or a resistor having a resistance of approximately between 0.2 ohms to 2.0 ohms.

23. (currently amended) The die assembly of claim 17 wherein the conductors comprises:

Appl. No. 10/720,812  
Amdt. Dated June 29, 2005

bumps attached to the passive element inductor to electrically connect the passive element inductor to at least one of the upper and lower dies.

24. (original) The die assembly of claim 17 wherein the lower die is attached to the package substrate by an adhesive between the lower die and the package substrate.

25. (withdrawn) A method comprising:  
integrating a resistor on a spacer between upper and lower dies in stacked dies; and  
attaching conductors to electrically connect the resistor to one of the upper and lower dies.

26. (withdrawn) The method of claim 25 further comprising:  
filling adhesive between the spacer and the upper die and between the spacer and the lower die.

27. (withdrawn) The method of claim 25 wherein integrating comprises:  
integrating the resistor being a thin-film resistor.

28. (withdrawn) The method of claim 25 wherein integrating comprises:  
integrating the resistor having a thickness substantially less than thickness of the spacer.

29. (withdrawn) The method of claim 28 wherein integrating comprises:  
integrating the resistor having a multi-turn geometry.

30. (withdrawn) The method of claim 25 wherein integrating comprises:  
integrating the resistor having a resistance of approximately between 0.2 ohm to 2 ohms.

31. (withdrawn) The method of claim 25 wherein attaching the conductors comprises:  
attaching bumps to electrically connect the resistor to at least one of the upper and lower dies.

Appl. No. 10/720,812  
Amdt. Dated June 29, 2005

32. (withdrawn) The method of claim 25 further comprising:  
filling adhesive between the lower die to a package substrate.
33. (original) A spacer assembly comprising:  
a resistor integrated on a spacer between upper and lower dies in stacked dies; and  
conductors attached to the resistor to connect the resistor to at least one of the upper and  
lower dies.
34. (original) The spacer assembly of claim 33 further comprising:  
adhesive layers filled between the spacer and the upper die and between the spacer and  
the lower die.
35. (original) The spacer assembly of claim 33 wherein the resistor is a thin-film  
resistor.
36. (original) The spacer assembly of claim 33 wherein the resistor has a thickness  
substantially less than thickness of the spacer.
37. (original) The spacer assembly of claim 36 wherein the resistor has a multi-turn  
geometry.
38. (original) The spacer assembly of claim 33 wherein the resistor has a resistance  
of approximately between 0.2 ohm to 2 ohms.
39. (original) The spacer assembly of claim 33 wherein the conductors comprises:  
bumps attached to the resistor to electrically connect the resistor to at least one of the  
upper and lower dies.
40. (original) The spacer assembly of claim 33 wherein the lower die is attached to a  
package substrate by an adhesive between the lower die and the package substrate.
41. (original) A die assembly comprising:

Appl. No. 10/720,812  
Amdt. Dated June 29, 2005

a package substrate;  
a plurality of stacked dies on the package substrate and having at least an upper die and a lower die; and  
at least a spacer assembly between the upper and lower dies, the spacer assembly comprising:  
a resistor integrated on a spacer between the upper and lower dies, and  
conductors attached to the resistor to electrically connect the resistor to at least one of the upper and lower dies.

42. (original) The die assembly of claim 41 wherein the spacer assembly further comprises:

adhesive layers filled between the spacer and the upper die and between the spacer and the lower die.

43. (original) The die assembly of claim 41 wherein the resistor is a thin-film resistor.

44. (original) The die assembly of claim 41 wherein the resistor has a thickness substantially less than thickness of the spacer.

45. (original) The die assembly of claim 44 wherein the resistor has a multi-turn geometry.

46. (original) The die assembly of claim 41 wherein the resistor has a resistance of approximately between 0.2 ohm to 2 ohms.

47. (original) The die assembly of claim 41 wherein the conductors comprises:  
bumps attached to the resistor to electrically connect the resistor to at least one of the upper and lower dies.

48. (original) The die assembly of claim 41 wherein the lower die is attached to the package substrate by an adhesive between the lower die and the package substrate.